

Remark

Applicants respectfully request reconsideration of this application as amended. Claims 1, 12, 13, 17, 24, 30, 31, 32, 38 and 38 have been amended. Claims 4, 10, 25, 26, 33 and 34 have been cancelled. Therefore, claims 1-3, 5-9, 11-24, 27-32 and 35-39 are present for examination.

Drawings

The drawings are objected to under 37 CFR 1.83 (a). With respect to the divider, the Examiner is respectfully referred to items 667 of Figure 6 and paragraph 54. A similar element is shown as 867 in Figure 8, and 967 in Figure 9.

With respect to the clocked circuit, paragraph 20 lists the seven internal clocking domains of the illustrated embodiment. In Figure 1, most of the domains are not shown in order to avoid obscuring the drawing. However, there is one domain that is shown and that is the Process Core Logic 119 that is coupled to the Core PLL. Note in paragraph 93, the specification states that a clocked circuit may be a processing core or some other circuit. There are also clocked circuits in the chips shown in Figure 5, which has been provided, at least in part, in the application to provide a context for the claimed system.

35 U.S.C. § 112 Rejection

Claims 17-23 stand rejected under 35 U.S.C. §112, second paragraph, as omitting essential structural cooperative relationships of elements. Claim 17 has been amended to address this objection, *inter alia*.

35 U.S.C. § 102 Rejection

Claims 1-4, 9-11, 13-16, 24-26, 28-34 and 36-39 are rejected under 35 U.S.C. §102(e) as being anticipated by Wyatt, U.S. Patent No. 7,002,358 ("Wyatt"). Note in the present application in paragraph 21, the specification states, "an external VCXO 127 provides a reference frequency on an external pin 129 to the I/O PLL. In this way, the I/O PLL provides the reference clock to the core PLL." Note also that external pin 129 is also labeled xxBCLK, indicating that this is also the "reference clock" input 213 of Figure 2.

In paragraph 25, the "bypass clock input may be used in order to bypass a single or multiple PLLs using the single input bypass clock and a reference clock input 213." The next paragraph (26) explains that the "reference clock may be generated by applying a divider to the bypass clock. In FIG. 2, this divider is external to the illustrated system." In paragraph 27, using "a bypass clock enable input 217, it selects either the bypass clock signal for bypass operation or the reference clock for normal operation."

The references do not relate to systems that have normal operation and bypass operation and the structure necessary to support these two types of operation. The claims have been amended to bring out this distinction and accordingly, the rejections based on the prior art are respectfully traversed.

35 U.S.C. § 103 Rejection

Claims 5-8, 12, 17-23, 27 and 35 are rejected under 35 U.S.C. §103(a) as being unpatentable over Wyatt, U.S. Patent No. 7,002,358 ("Wyatt"), in view of Cox et al., U.S. Patent No. 6,583,679 ("Cox"). These references also do not relate to systems that have normal operation and bypass operation and the structure necessary to support these two types of operation. This rejection is also traversed on the grounds provided above.

Conclusion

Applicants respectfully submit that the rejections have been overcome by the amendment and remark, and that the claims as amended are now in condition for allowance. Accordingly, Applicants respectfully request the rejections be withdrawn and the claims as amended be allowed.

Invitation for a Telephone Interview

The Examiner is requested to call the undersigned at (303) 740-1980 if there remains any issue with allowance of the case.

Request for an Extension of Time

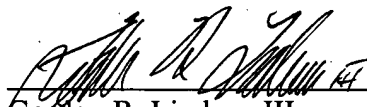
Applicants respectfully petition for an extension of time to respond to the outstanding Office Action pursuant to 37 C.F.R. § 1.136(a) should one be necessary. Please charge our Deposit Account No. 02-2666 to cover the necessary fee under 37 C.F.R. § 1.17(a) for such an extension.

Charge our Deposit Account.

Please charge any shortage to our Deposit Account No. 02-2666.

Respectfully submitted,
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